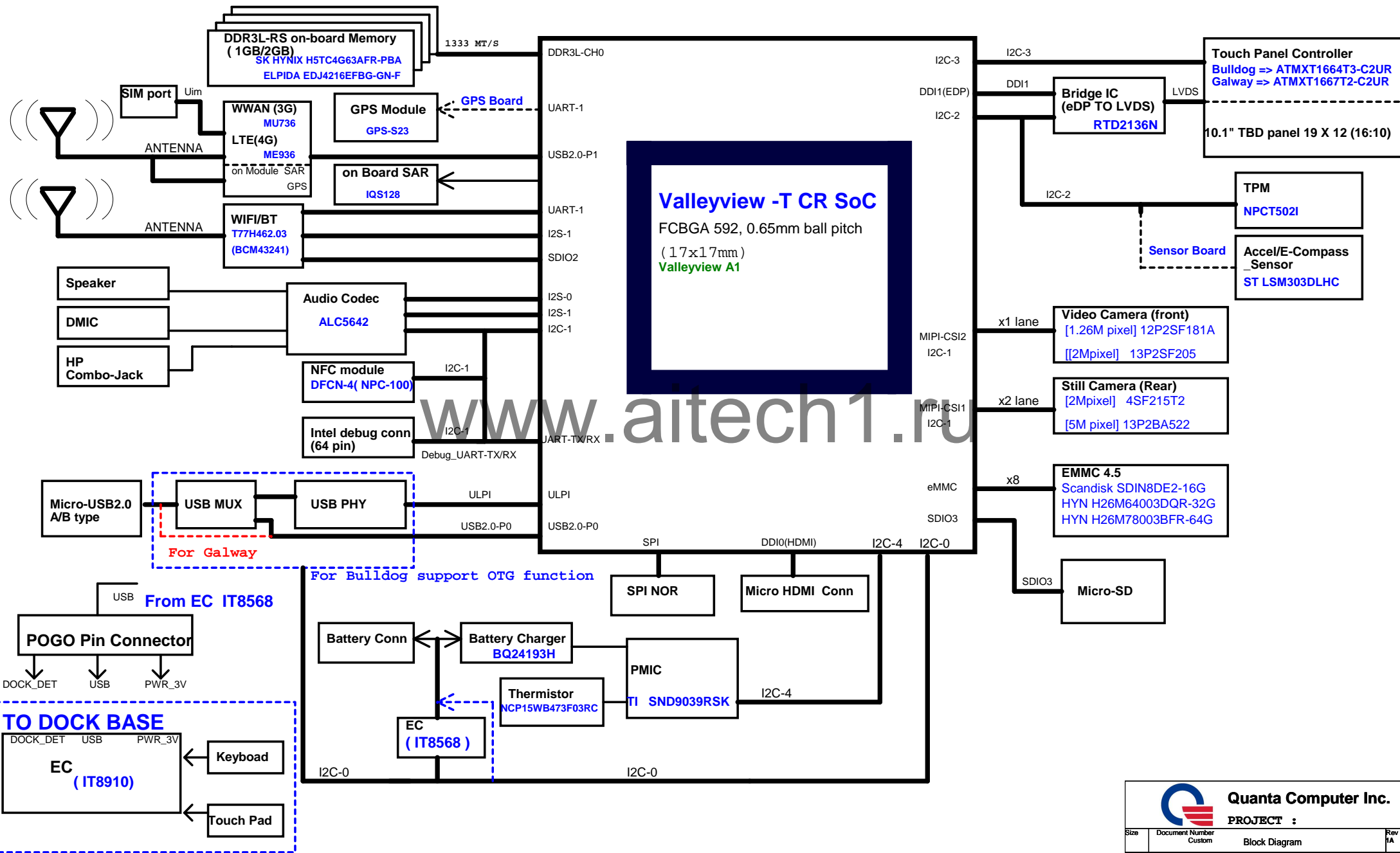
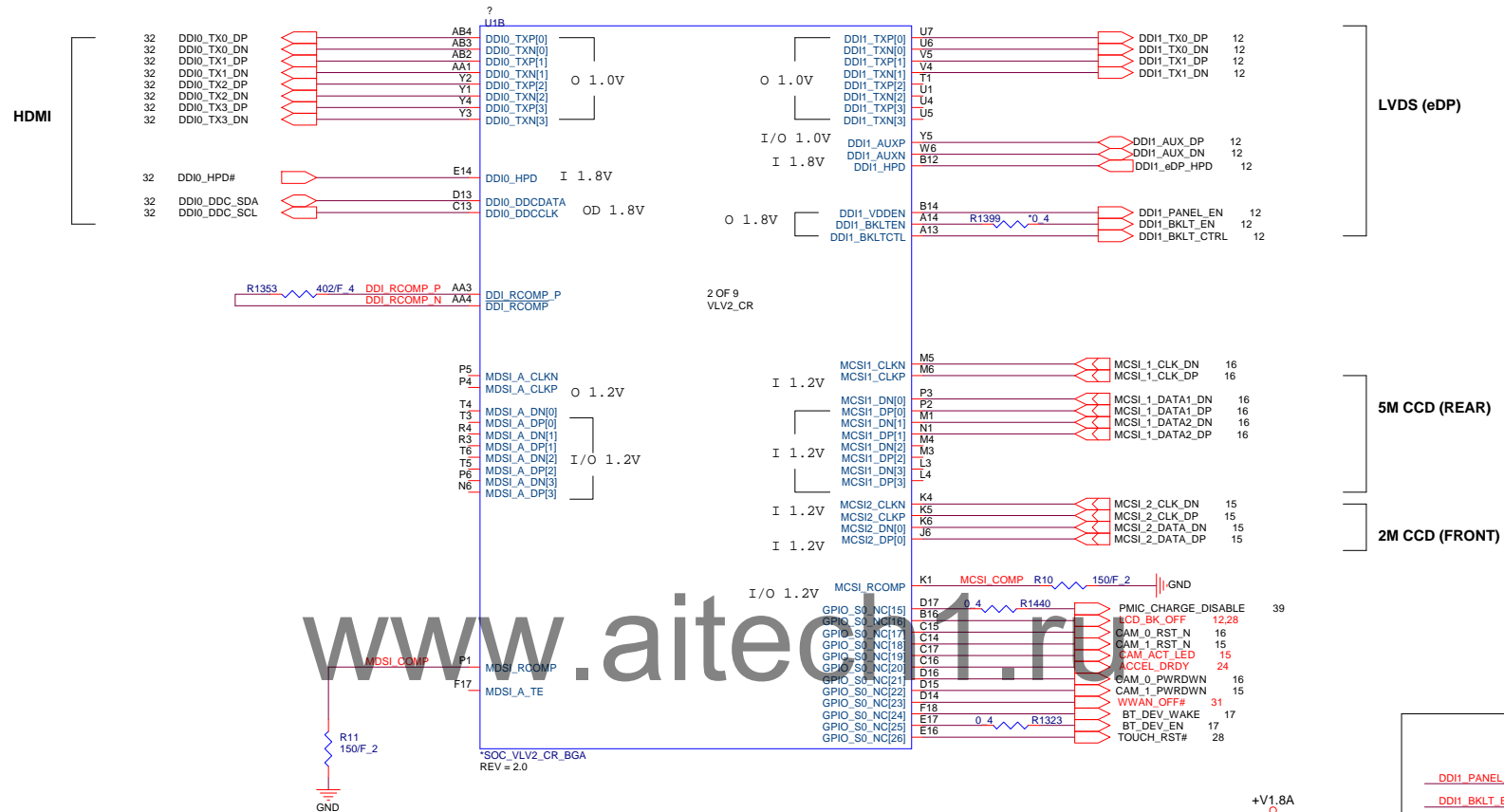



Galway / Bulldog Block Diagram (Intel Bay Trail-T CR Platform)

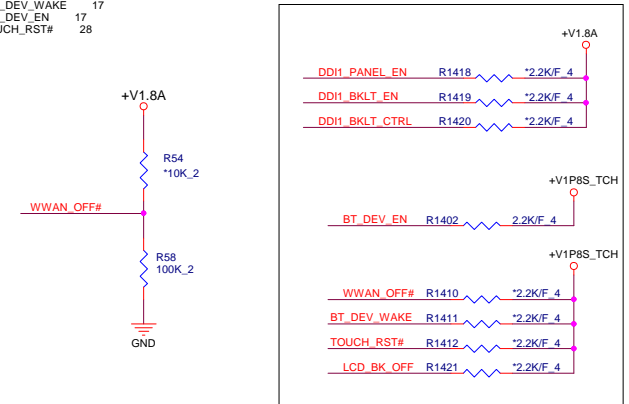


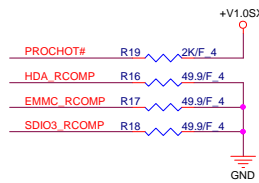
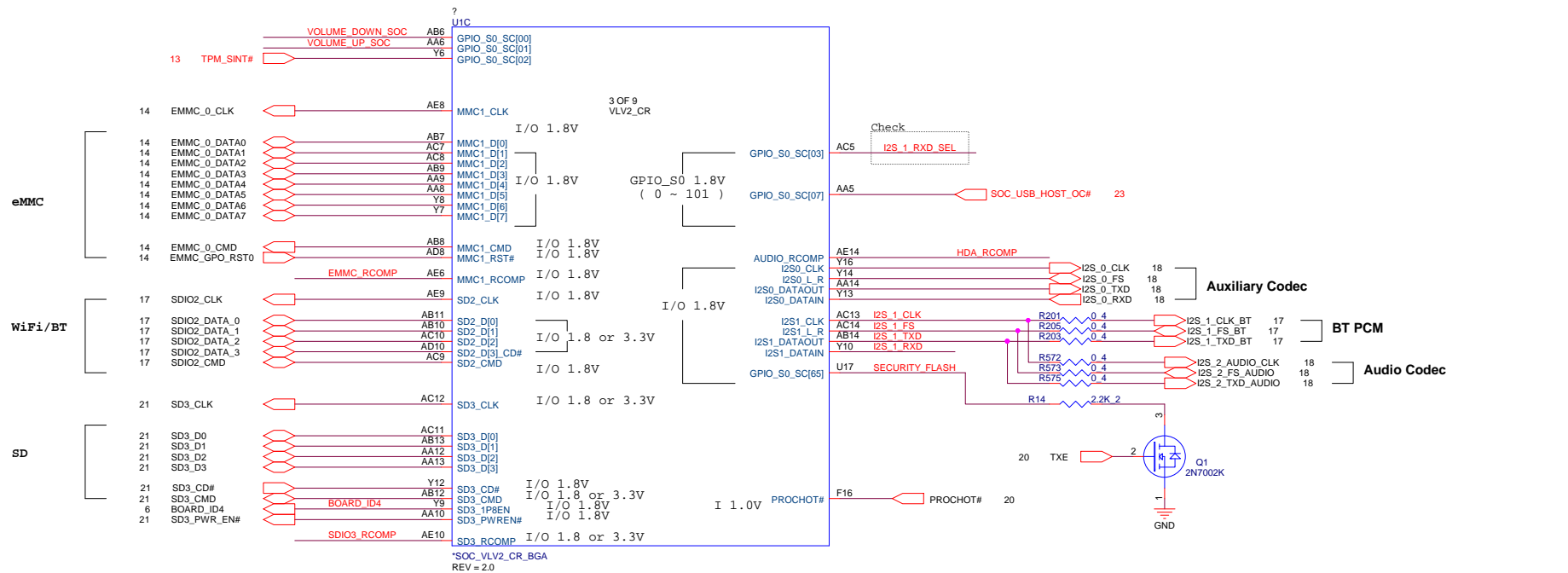




Hardware Straps (EDS 2P2)

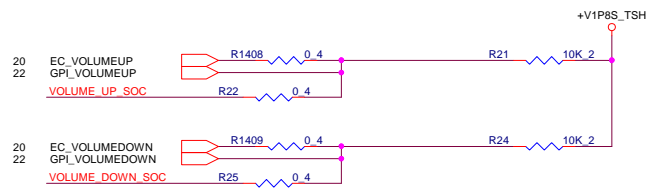
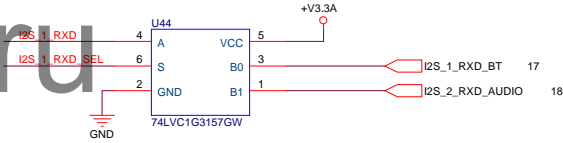
Pin Name	Purpose	Polarity	Internal PU/PD	
DDIO_DDCDATA	DDIO Detect	0 = DDIO not detected 1 = DDIO detected	PD	+V1.8A 



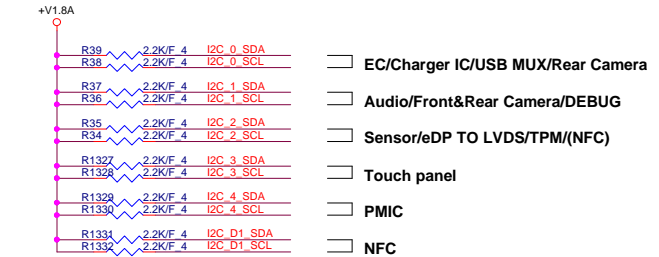
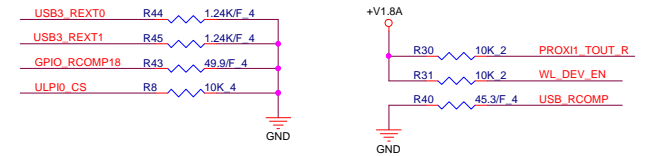
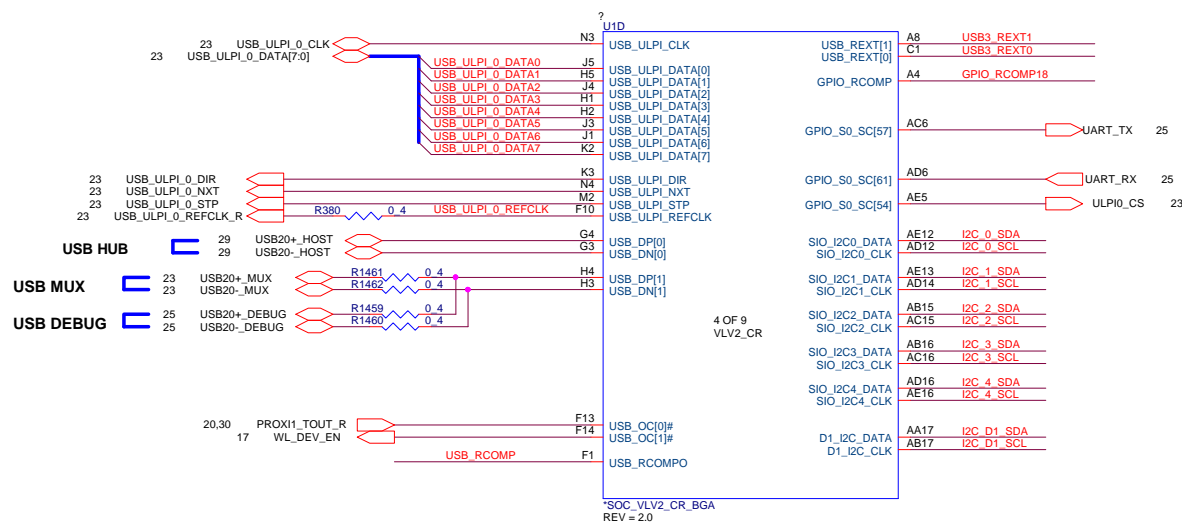


Hardware Straps (EDS 2P2)

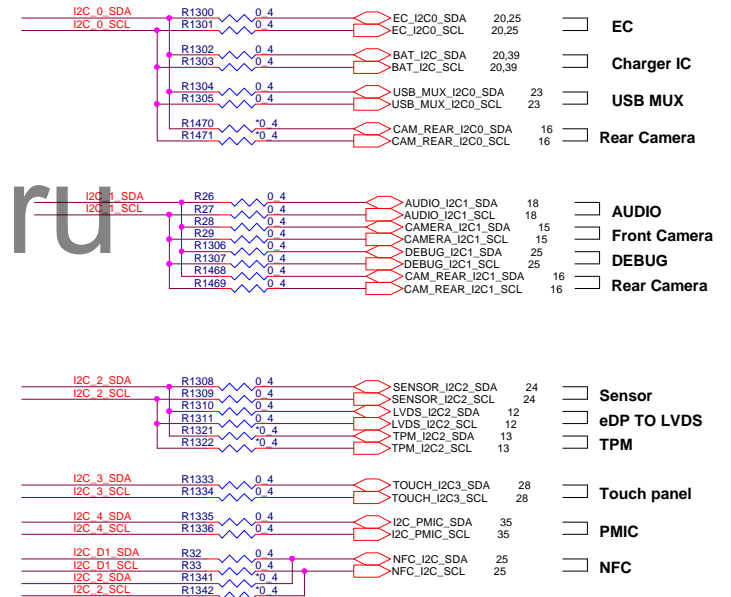
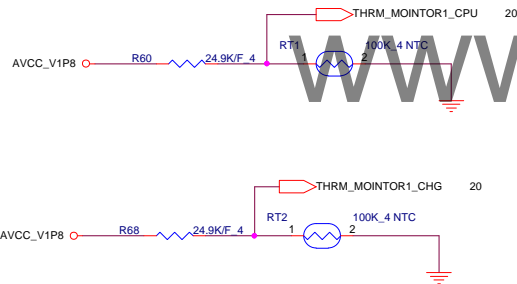
Pin Name	Purpose	Polarity	Internal PU/PD	
GPIO_S0_SC[65]	Security Flash Descriptors	0 = Override 1 = Normal Operation	PU	+V1.8A



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IO Thrm Protect

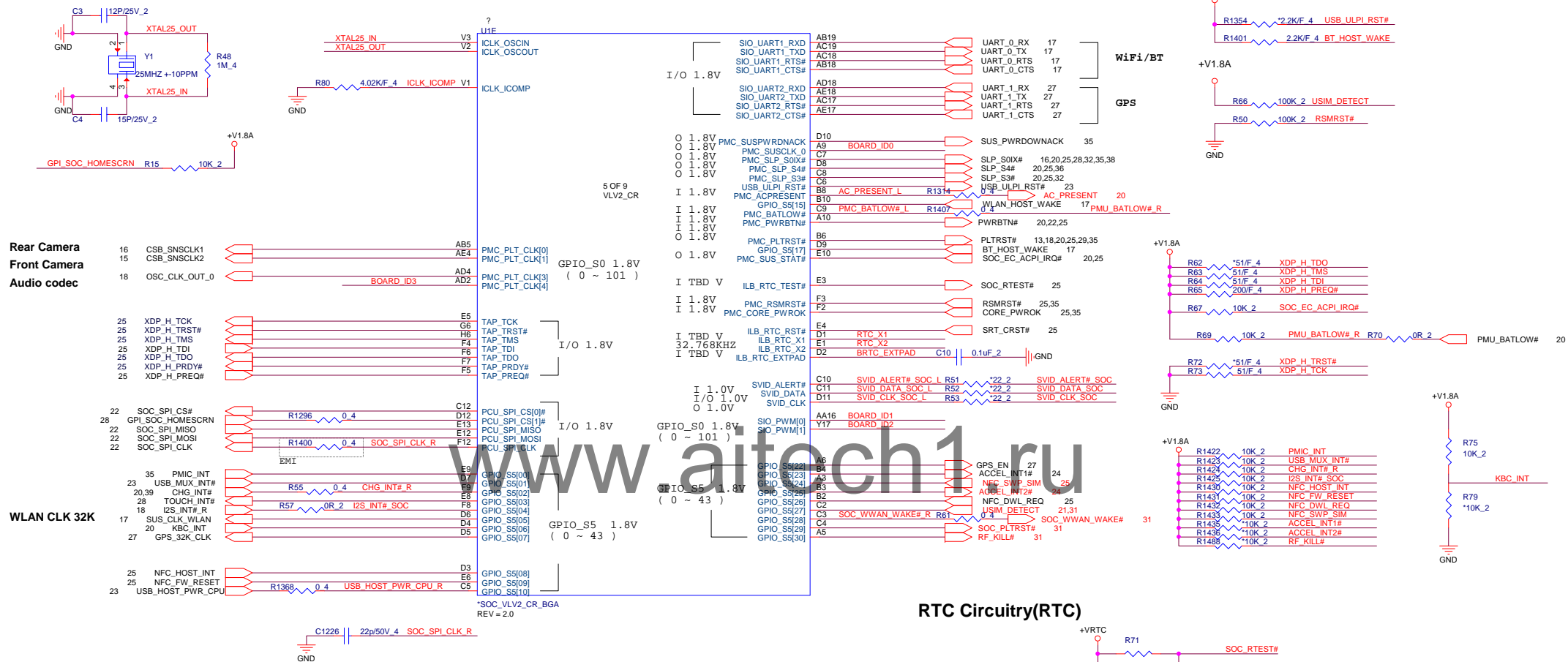


Port	Usage
SIO_I2C_0	Charger, Fuel Gauge, USB Mux.
SIO_I2C_1	Camera [2:1] EEPROM, Camera Flash, Audio Codec, I2C Debug (Post Codes)
SIO_I2C_2	Accelerator/Compass, ALS, GYRO, NFC, LVDS Bridge, MIPI Panel
SIO_I2C_3	Touch
SIO_I2C_4	PMIC

3,4,6,8,14,17,18,19,20,22,23,24,25,27,28,31,32,34,35
4,8,13,15,18,24,25,27,30,31,35,38

+V1.8A
+V3.3A

Quanta Computer Inc. PROJECT :			
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Date:	Saturday, June 28, 2014	Sheet	5 of 44



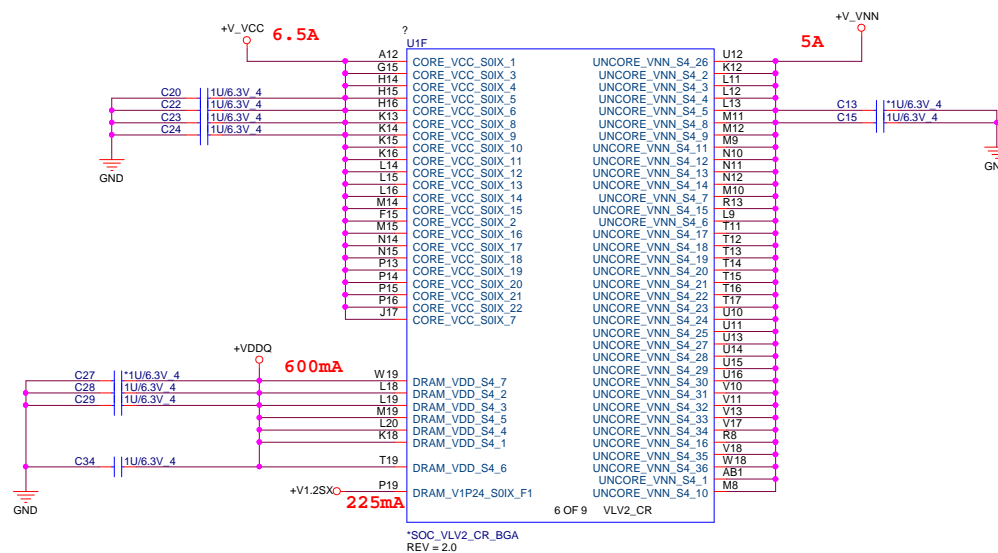
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12,17,20,24,25,27,28,29,36

+V1.8A

+VRTC

+V3.3A_SRC



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8,18,25,35 +V1.2SX
34 +V_VNN
34 +V_VCC
2,10,11,34,38 +VDDQ
18,23,32,36,37 +VSBAT_SPKR




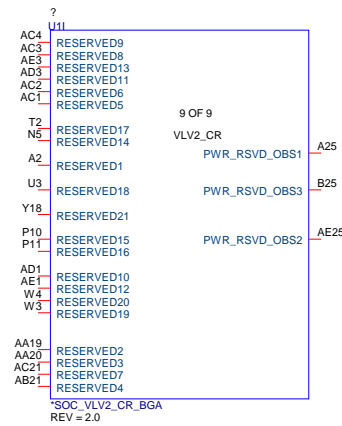
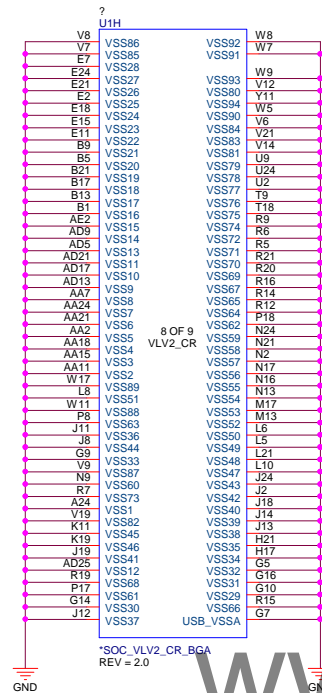
Quanta Computer Inc.

PROJECT :

Size	Document Number	Custom	CPU (6/8)	Rev
				1A
Date: Saturday, June 28, 2014 Sheet 7 of 44				



 Quanta Computer Inc. PROJECT :	
Size	Document Number Custom CPU (7/8)
Date: Saturday, June 28, 2014	Sheet 8 of 44



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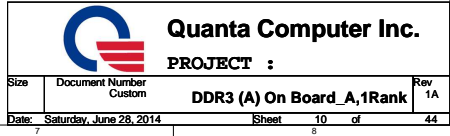


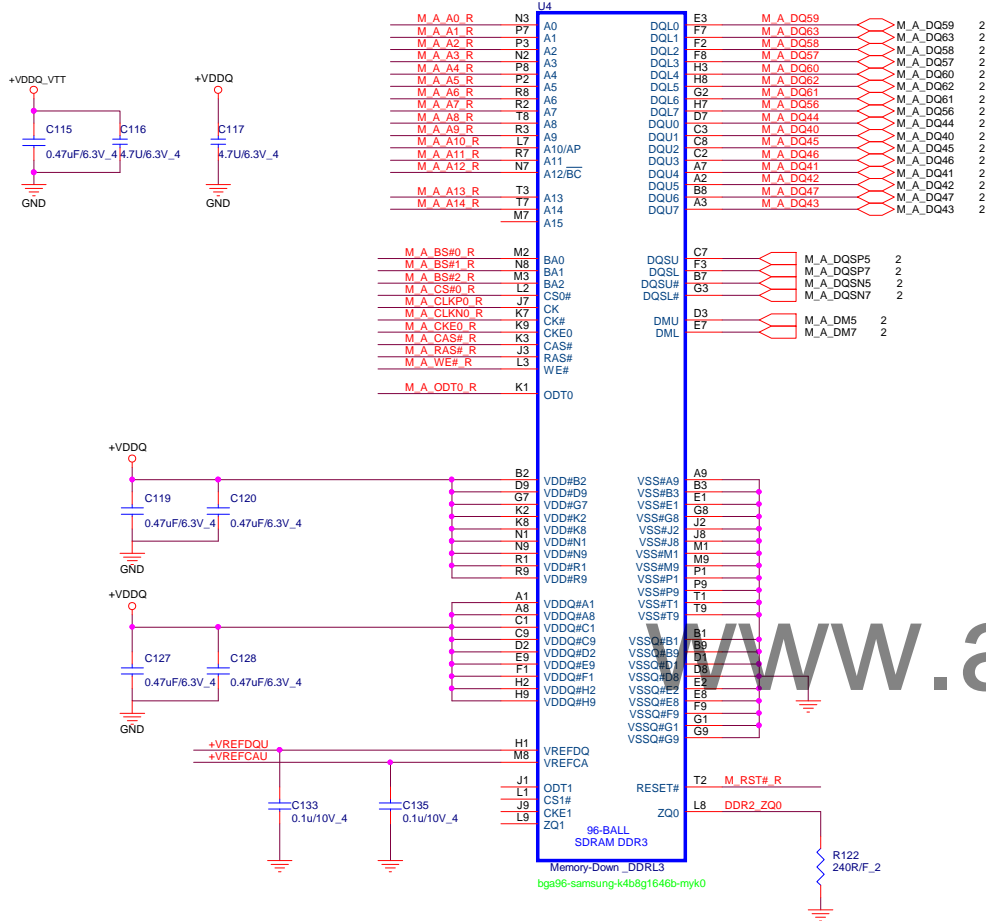
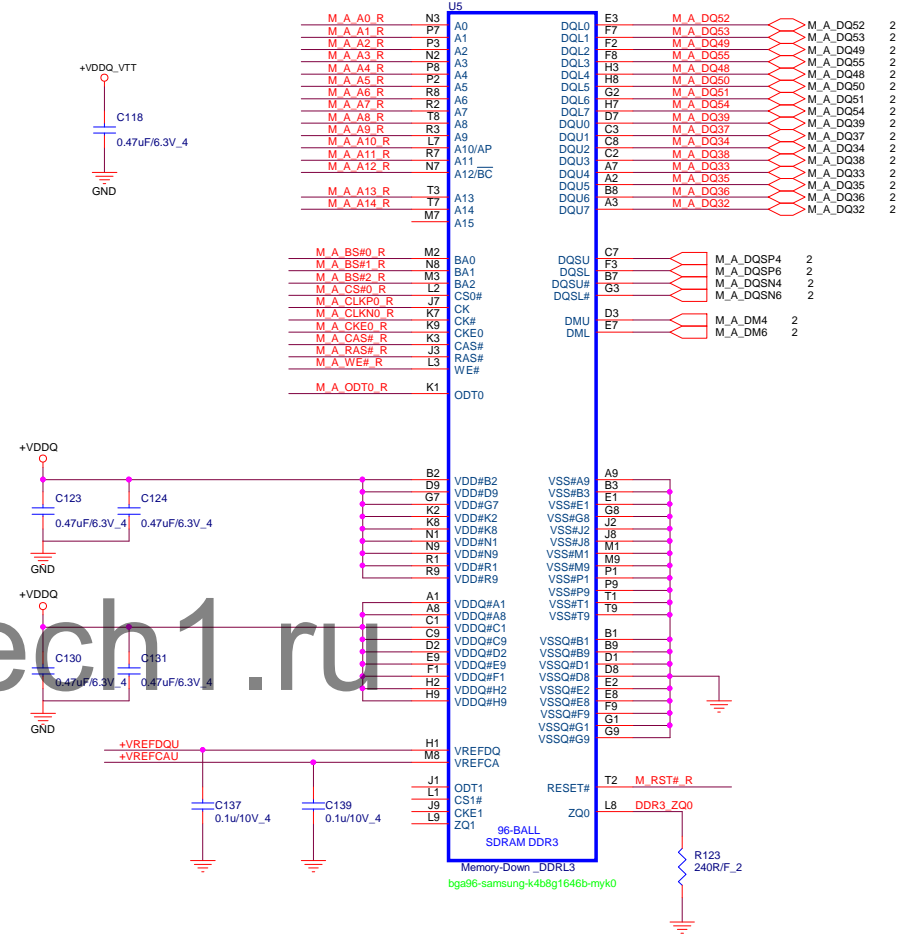
Quanta Computer Inc.

PROJECT :

Size	Document Number	Custom	CPU (8/8)	Rev	1A
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Date: Saturday, June 28, 2014 Sheet 9 of 44



11.0mm x 13.3mm
D211.0mm x 13.3mm
D3

2.10	M_A_BS#0	R591	OR_2	M_A_BS#0_R
2.10	M_A_BS#1	R592	OR_2	M_A_BS#1_R
2.10	M_A_BS#2	R593	OR_2	M_A_BS#2_R
2.10	M_A_CS#0	R594	OR_2	M_A_CS#0_R
2.10	M_A_CLKP0	R595	OR_2	M_A_CLKP0_R
2.10	M_A_CLKN0	R596	OR_2	M_A_CLKN0_R
2.10	M_A_CKE0	R597	OR_2	M_A_CKE0_R
2.10	M_A_CAS#	R598	OR_2	M_A_CAS#_R
2.10	M_A_RAS#	R599	OR_2	M_A_RAS#_R
2.10	M_A_WE#	R600	OR_2	M_A_WE#_R
2.10	M_A_ODT0	R601	OR_2	M_A_ODT0_R
2.10	M_RST#	R602	OR_2	M_RST#_R
2.10	M_A_A0	R603	OR_2	M_A_A0_R
2.10	M_A_A1	R604	OR_2	M_A_A1_R
2.10	M_A_A2	R605	OR_2	M_A_A2_R
2.10	M_A_A3	R606	OR_2	M_A_A3_R
2.10	M_A_A4	R607	OR_2	M_A_A4_R
2.10	M_A_A5	R608	OR_2	M_A_A5_R
2.10	M_A_A6	R609	OR_2	M_A_A6_R
2.10	M_A_A7	R610	OR_2	M_A_A7_R
2.10	M_A_A8	R611	OR_2	M_A_A8_R
2.10	M_A_A9	R612	OR_2	M_A_A9_R
2.10	M_A_A10	R613	OR_2	M_A_A10_R
2.10	M_A_A11	R614	OR_2	M_A_A11_R
2.10	M_A_A12	R615	OR_2	M_A_A12_R
2.10	M_A_A13	R616	OR_2	M_A_A13_R
2.10	M_A_A14	R617	OR_2	M_A_A14_R

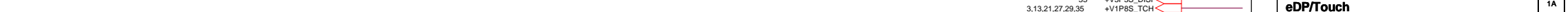
SIZE	Vendor	Vendor PN	QBCON PN	TOPBS PN	BOARD ID[0:2]
2GB	Hynix	H5TC4G63AFR-PBA	AKD5JGETW08	AKD5JGETW07	000
	Micron	MT41K256M16HA-125:E	AKD5JGSTL07	AKD5JGSTL06	001
	Samsung	K4B4G1646Q-HYK0	AKD5PGST509	AKD5PGST508	010

41 TX00OUT+ +V3P3S_DISP +3.3V_2136_A

2 DDH BKLT CTRL R129 0.4 RTD2316_PWMIN +V3P3S_DISP +3.3V_2136_A



+V3P3S_DISP +3.3V_2136_A

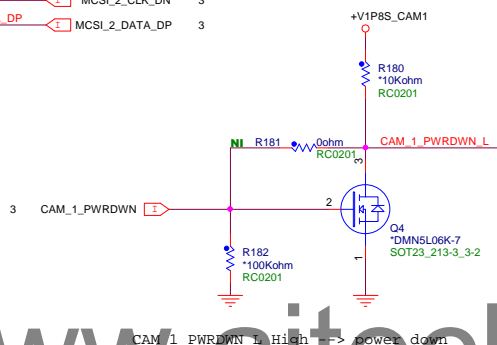


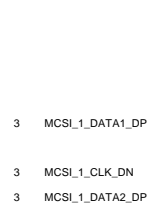
For EMI

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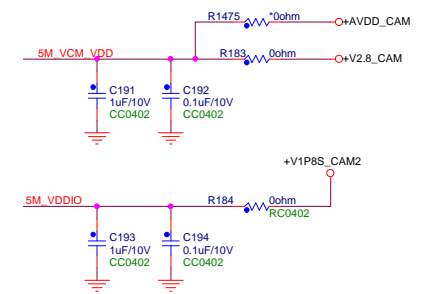
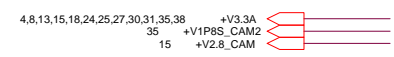


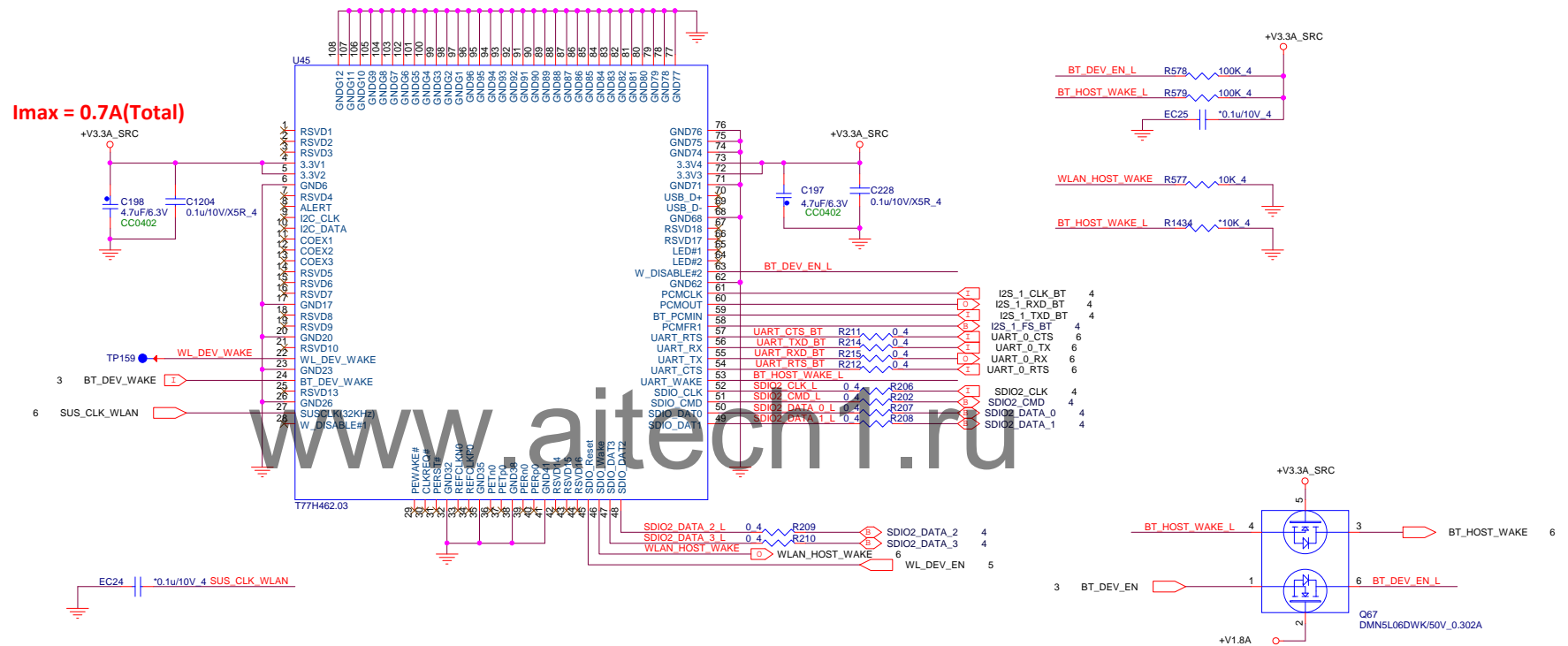
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35 +V2P85S_MMC

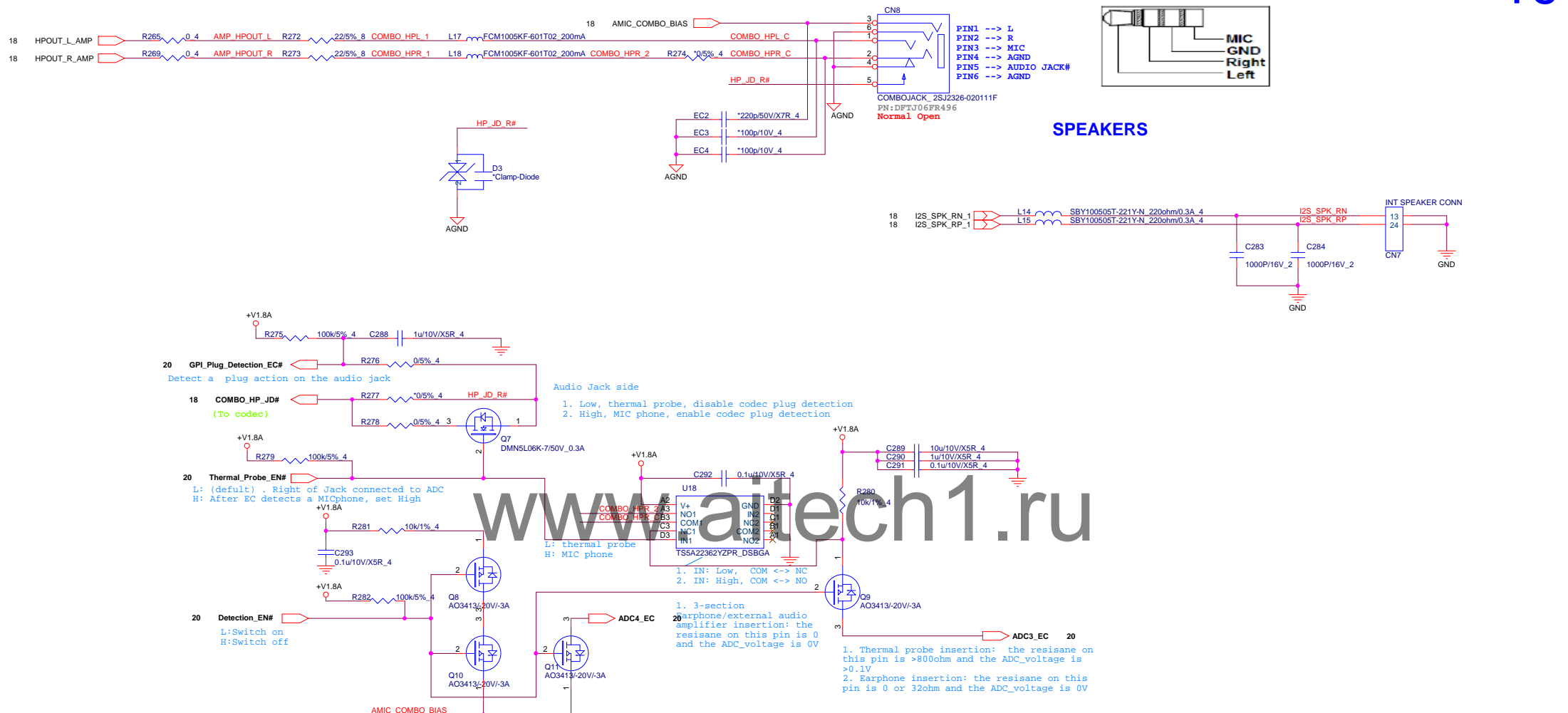




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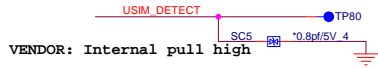




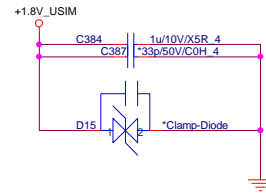


SIM Card

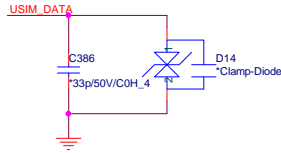
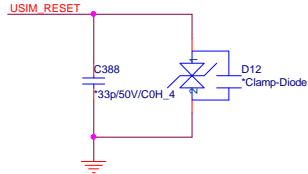
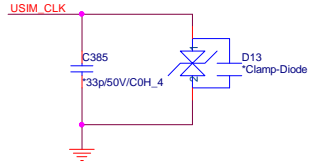
ESD



USIM POWER



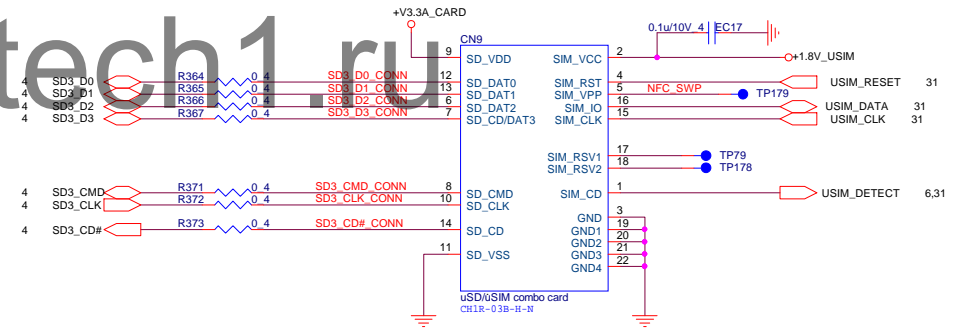
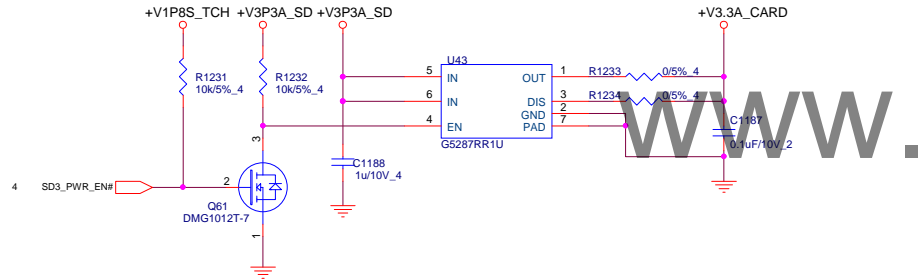
USIM DATA



SD3_D0	C19	*5.6PF/50V_2
SD3_D1	C21	*5.6PF/50V_2
SD3_D2	C25	*5.6PF/50V_2
SD3_D3	C26	*5.6PF/50V_2
SD3_CMD	C30	*5.6PF/50V_2
SD3_CLK	C31	*5.6PF/50V_2
SD3_CD#	C32	*5.6PF/50V_2

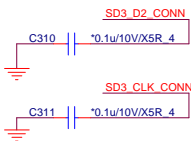
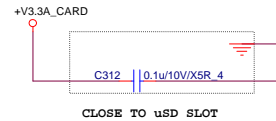
SD3_D0_CONN	C33	*5.6PF/50V_2
SD3_D1_CONN	C35	*5.6PF/50V_2
SD3_D2_CONN	C36	*5.6PF/50V_2
SD3_D3_CONN	C37	*5.6PF/50V_2
SD3_CMD_CONN	C43	*5.6PF/50V_2
SD3_CLK_CONN	C45	*5.6PF/50V_2
SD3_CD#_CONN	C46	*5.6PF/50V_2

uSD/uSIM COMBO



EMI	SD3_D0	EC5	*10p/50V/NPO_4
	SD3_D1	EC7	*10p/50V/NPO_4
	SD3_D2	EC9	*10p/50V/NPO_4

	SD3_D3	EC6	*10p/50V/NPO_4
	SD3_CLK_CONN	EC8	*10p/50V/NPO_4



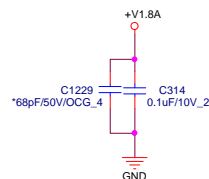
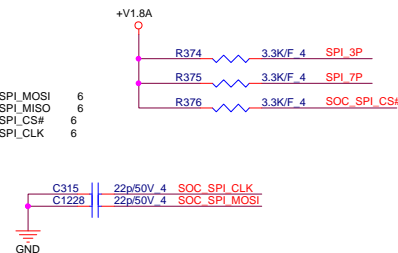
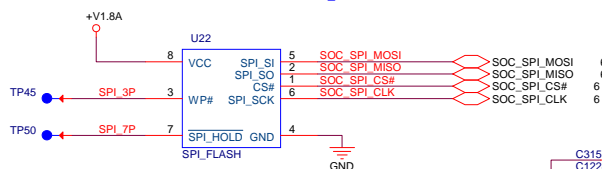
20,27,28,36,39	+1.8VPCU_EC
35	+V3P3A_SD
4,8,13,15,18,24,25,27,30,31,35,38	+V3.3A

Quanta Computer Inc.
PROJECT : Bulldog

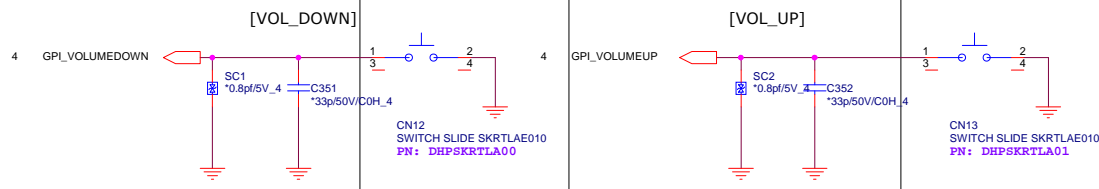
Size	Document Number	Ext. uSD	Rev. 1A
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SPI NOR FLASH

SOC_SPI_MOSI → TP46
 SOC_SPI_MISO → P47
 SOC_SPI_CS# → P48
 SOC_SPI_CLK → P49

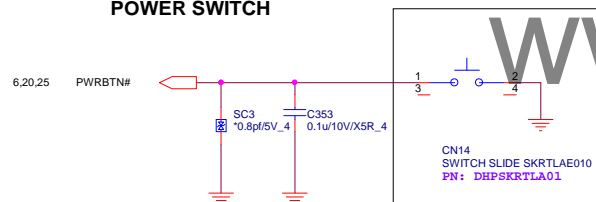
CHANGE TO W25Q16DWZPIG
8M/2M Co-layout

VOLUME CONTROL



Vender	Size	P/N
Winbond	8MB	AKE5EZKN01 (W25Q64FWZPIG)
Socket		DFHS08FS023

POWER SWITCH

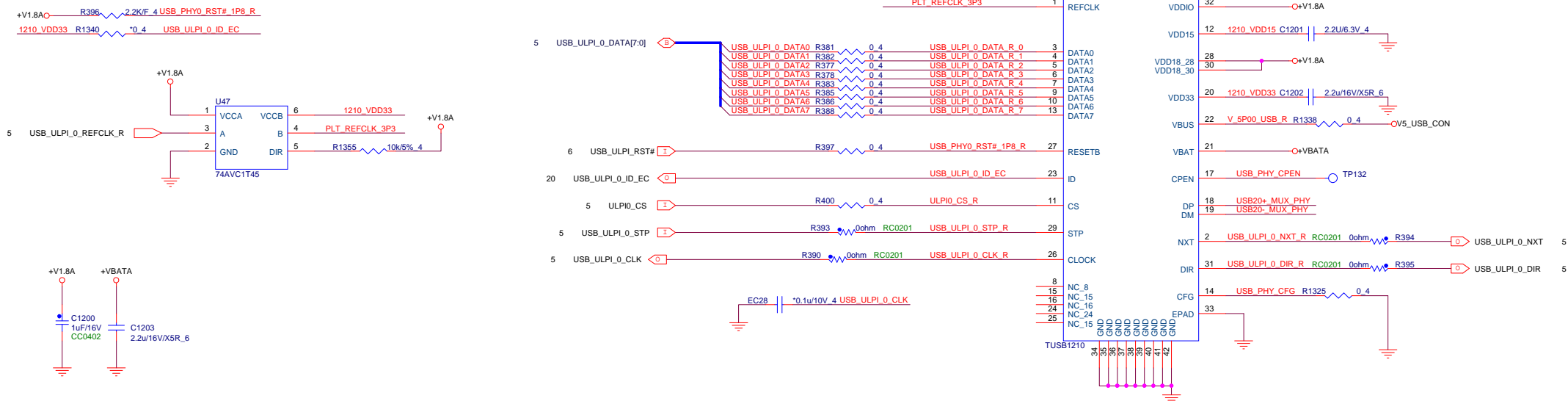


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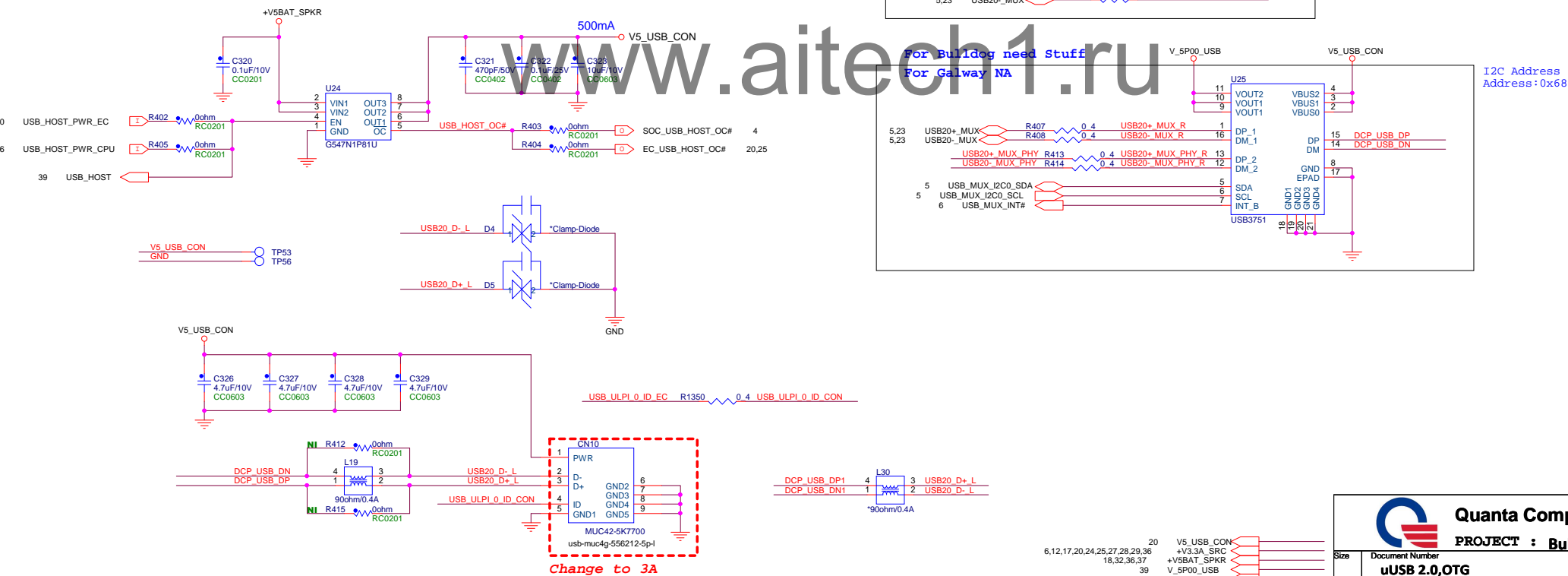
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 6,12,17,20,24,25,27,28,29,36

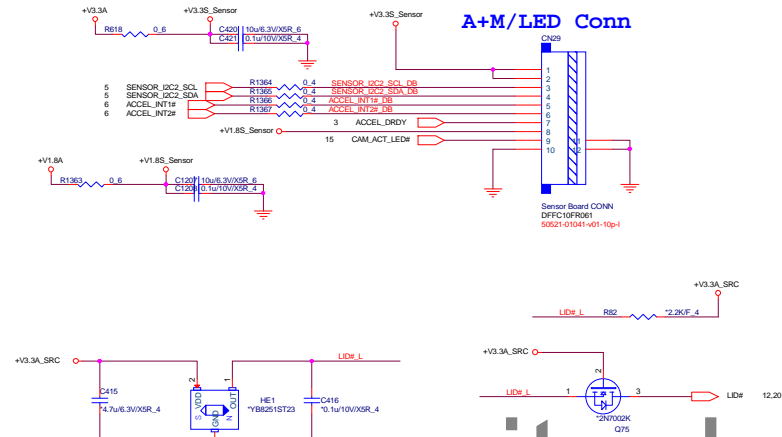
+V1.8A
 +V3.3A_SRC

USB PHY

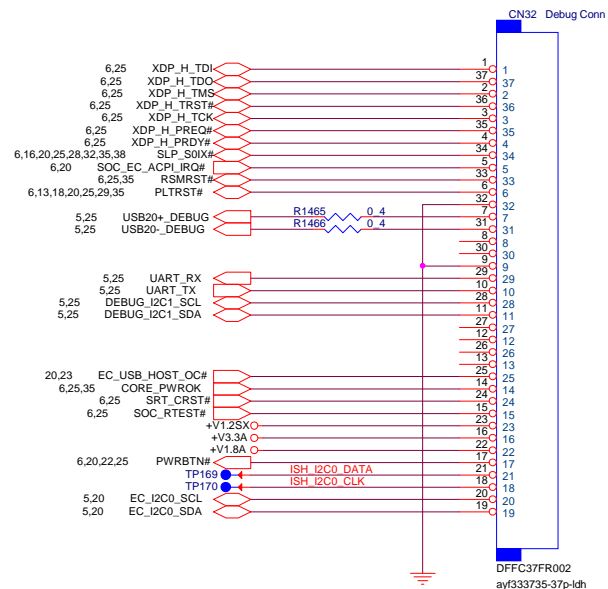


USB MUX





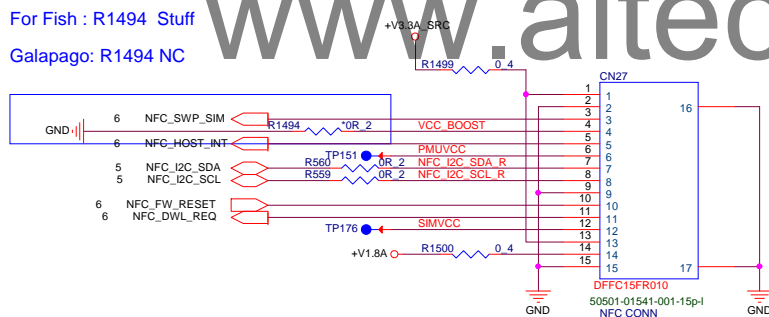
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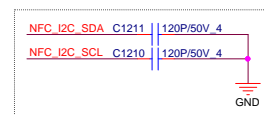
NFC

For Fish : R1494 Stuff
Galapago: R1494 NC

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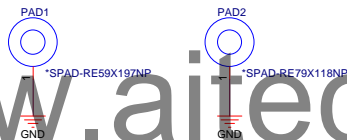
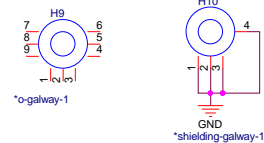
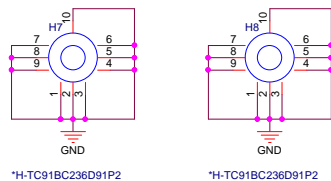
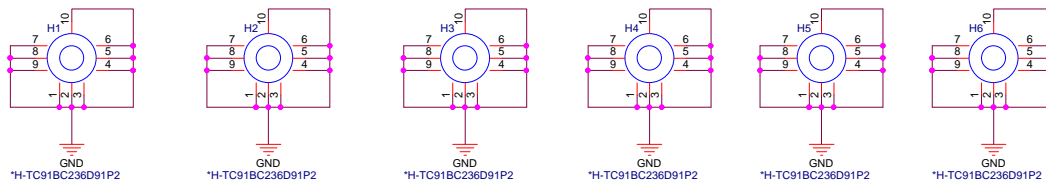


EMI

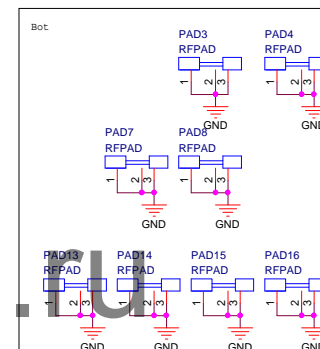
**Quanta Computer Inc.**

PROJECT :

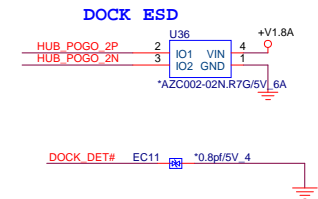
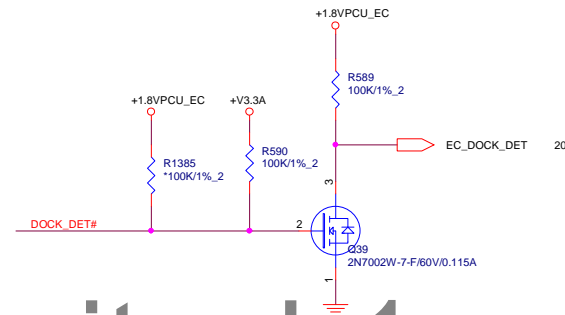
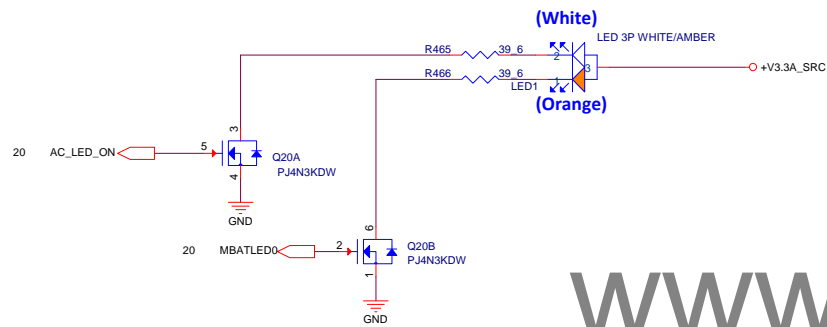
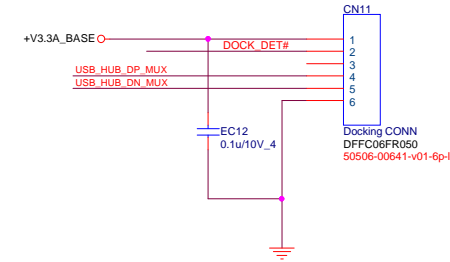
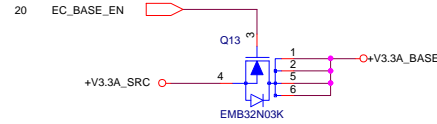
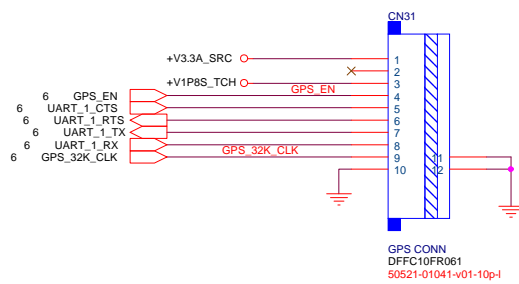
Size	Document Number Custom	Rev 1A
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RF



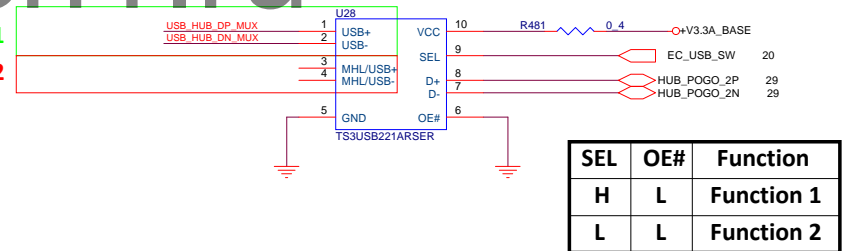
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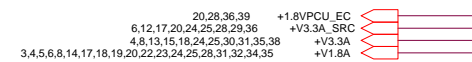
www.aitech1.ru

Function 1

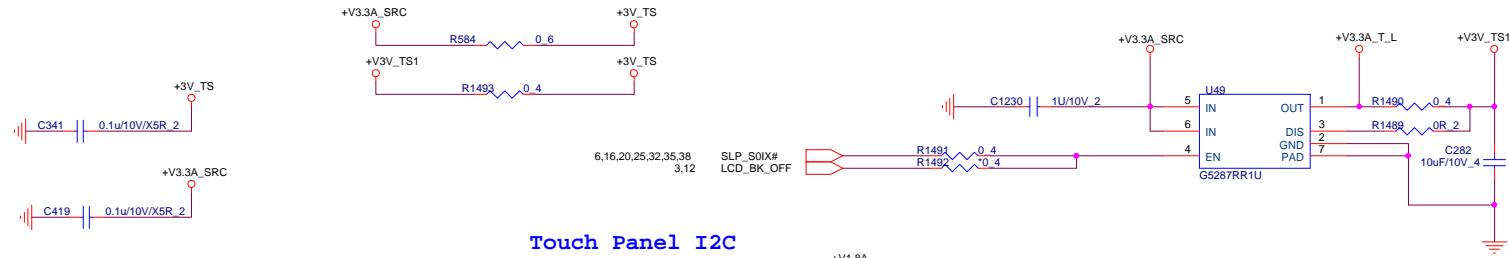
Function 2



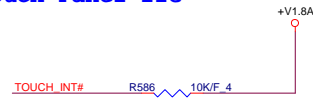
SEL	OE#	Function
H	L	Function 1
L	L	Function 2



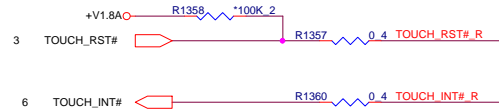
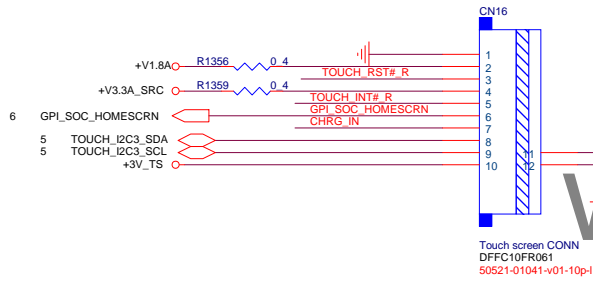
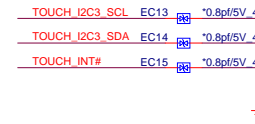
Touch Panel VCC Control



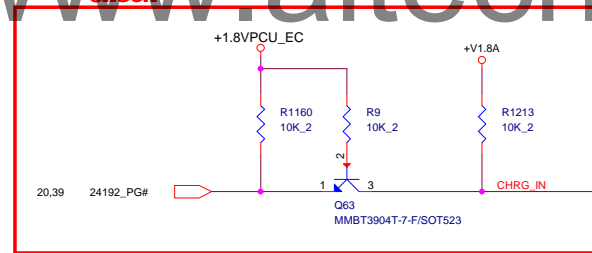
Touch Panel I2C

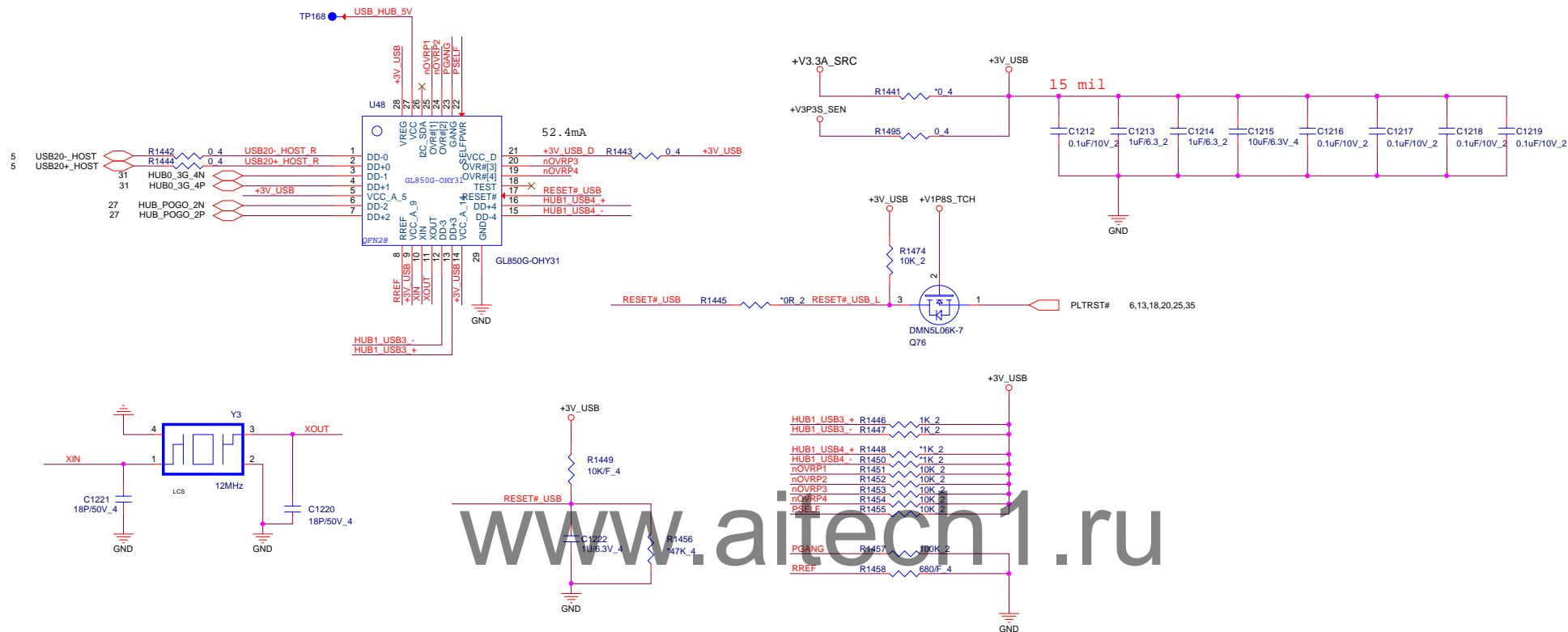


Touch Panel ESD



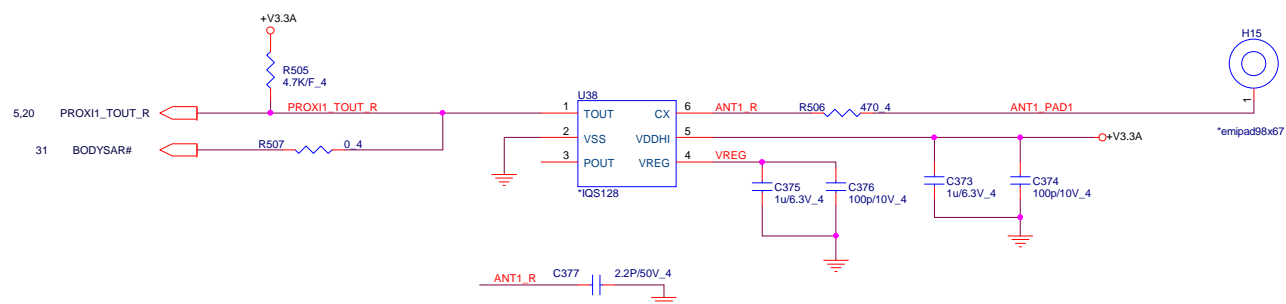
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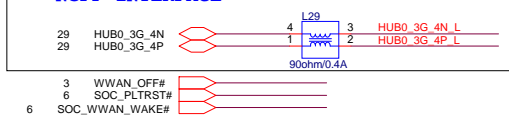
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4,8,13,15,18,24,25,27,31,35,38

+V3.3A

Check



TP142 SATA TXP0 C
TP143 SATA TXN0 C
TP144 SATA RXP0 C
TP145 SATA RXN0 C

SOC_PLTRST# R517 0.4 NGFF_RST#

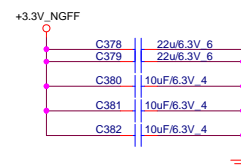
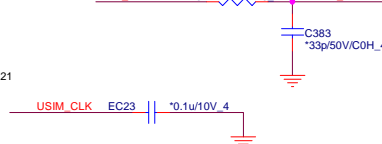
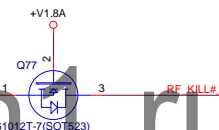
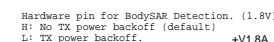
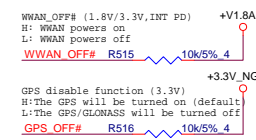


Diagram illustrating the WWAN_WAKE# signal path. The signal is pulled up to +V1.8A by a resistor Q25 (DTC144EUA/50V/0.03A). The signal line passes through a series resistor R519 (0.5% 4) and is connected to the WWAN_WAKE# pin of the SOC.



RF_KILL# (3.3V)
H: RF is turned on (default)
L: RF is turned off

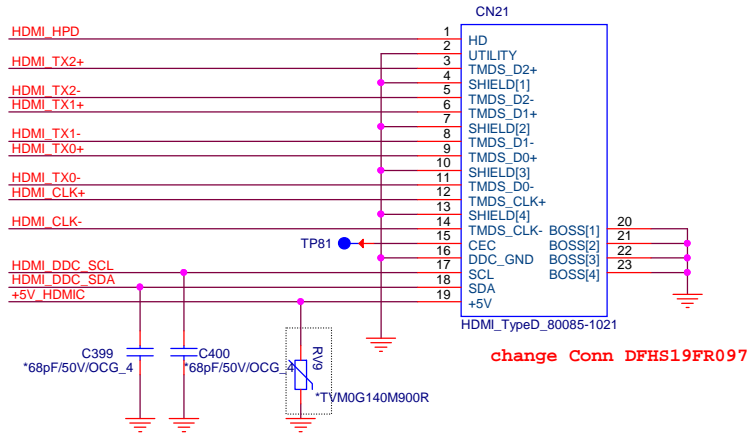
RF KILL# L R514 10k/5% 4



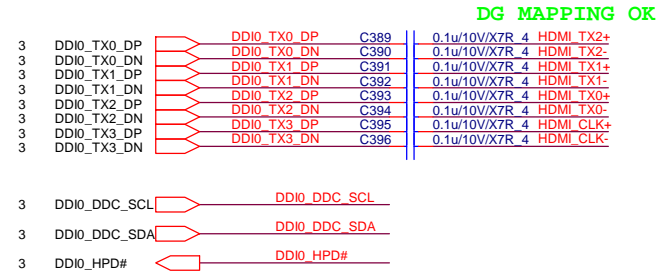
WWAN/SSD_IND, Slot B Indication Pin Definitions
GND: SSD SATA
NC: WWAN
WWAN/SSD_IND R520 *0/5% 4



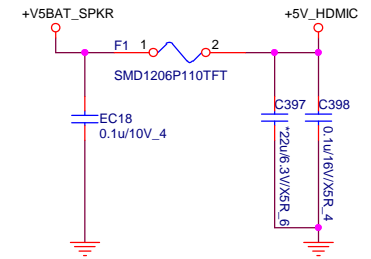
HDMI CONNECTOR (D TYPE)



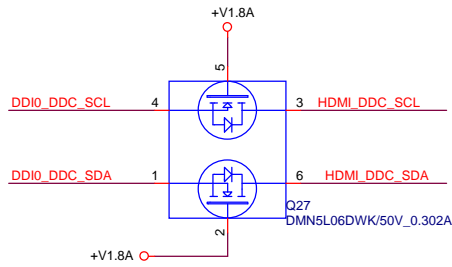
HDMI INTERFACE



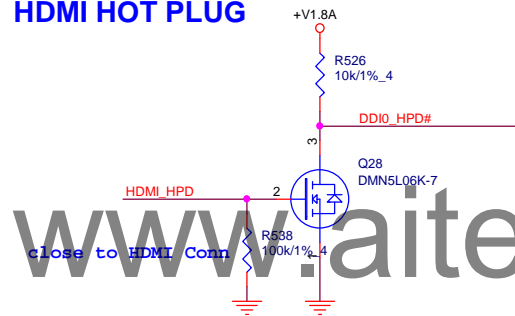
HDMI POWER SUPPLY



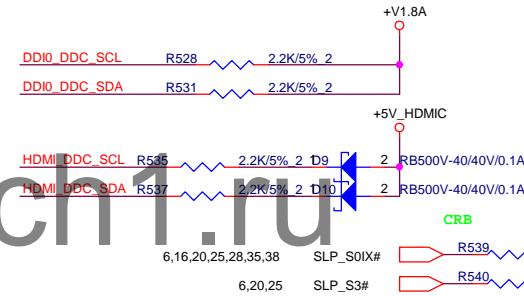
I2S BUS LEVEL SHIFT



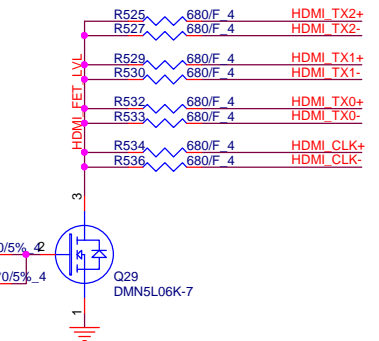
HDMI HOT PLUG



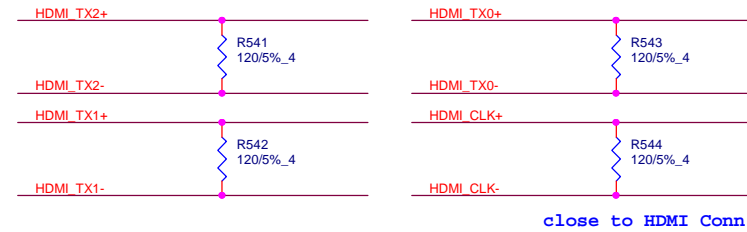
I2C Pull up

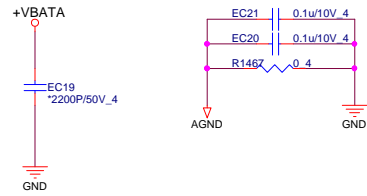


HDMI LEVEL SHIFT



EMI reserve for HDMI





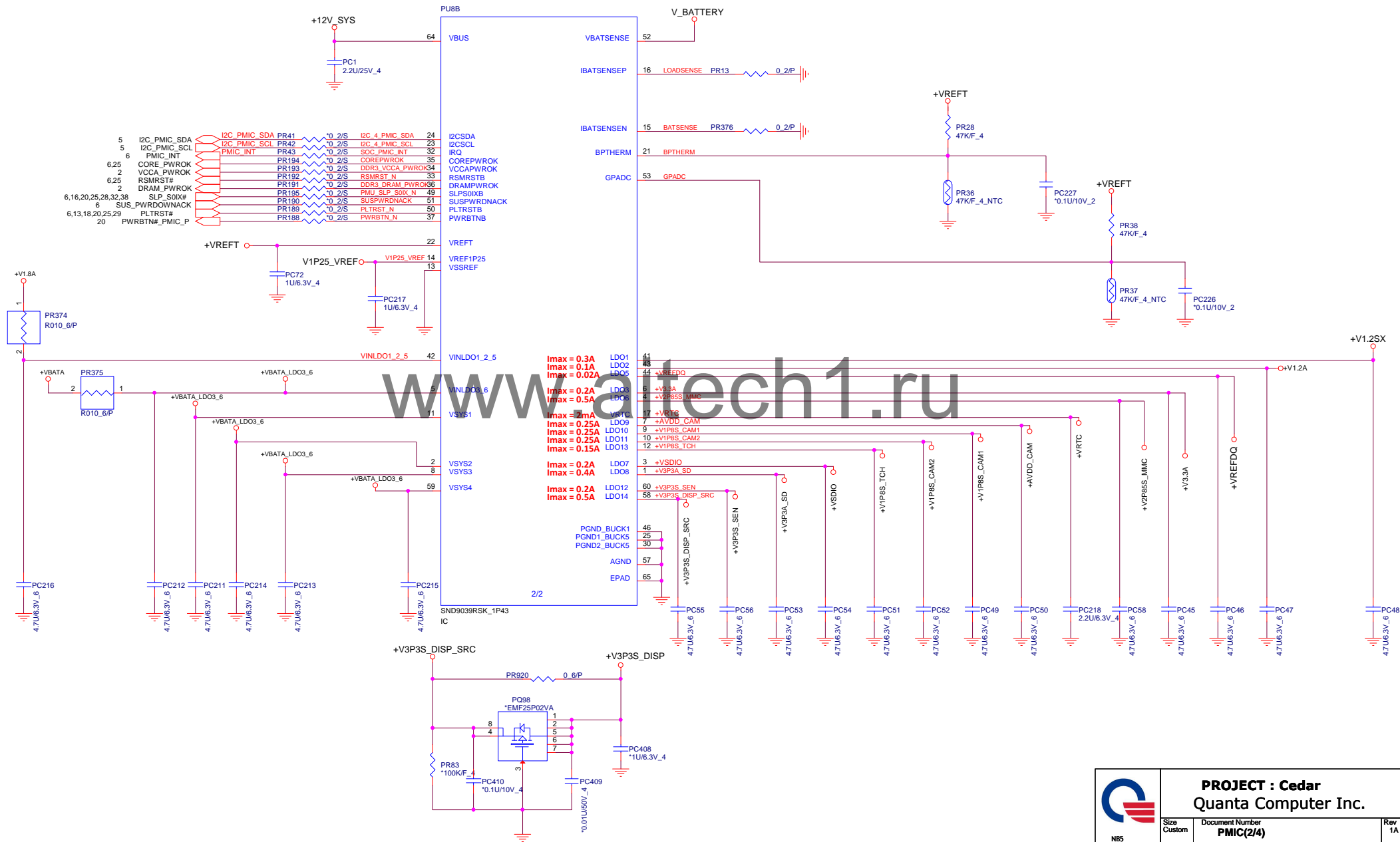
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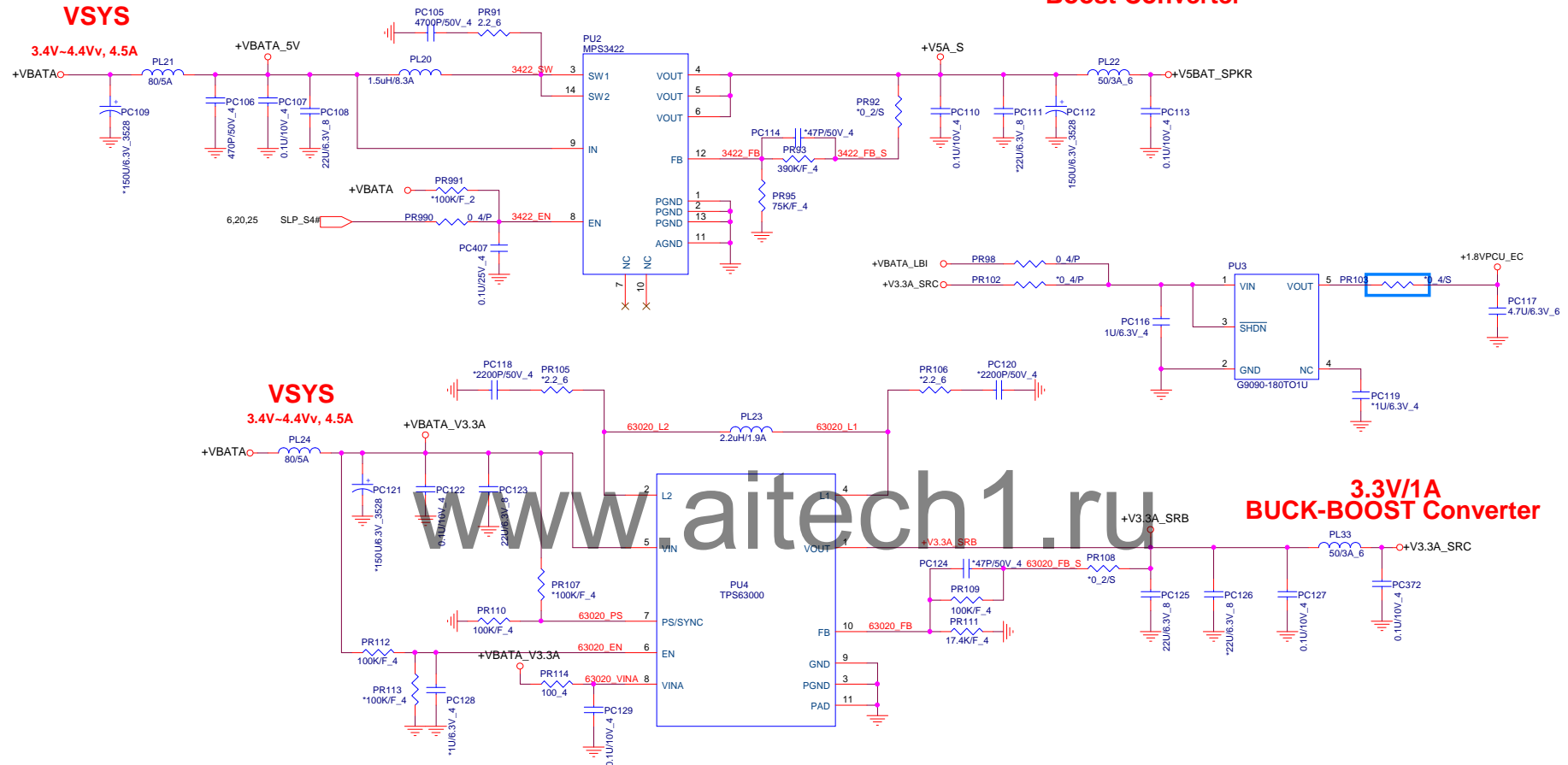
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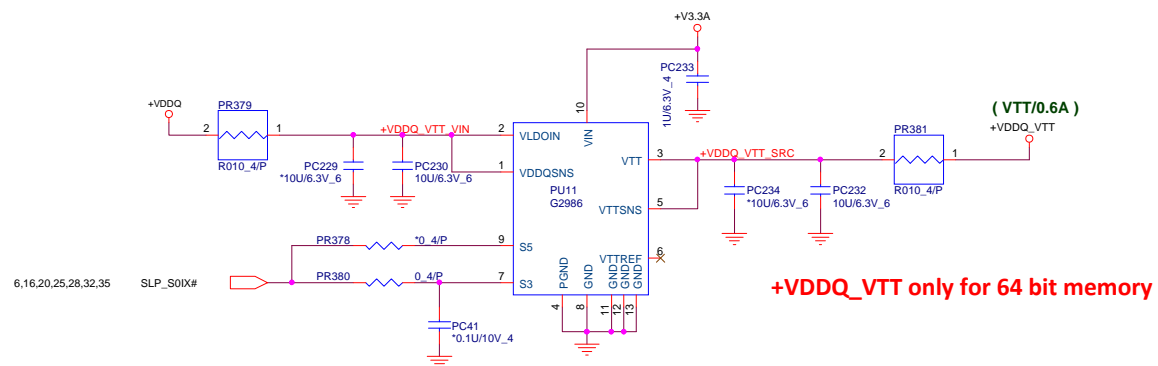
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Level Shifter		
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5V/2A
OCP: 3A
Boost Converter





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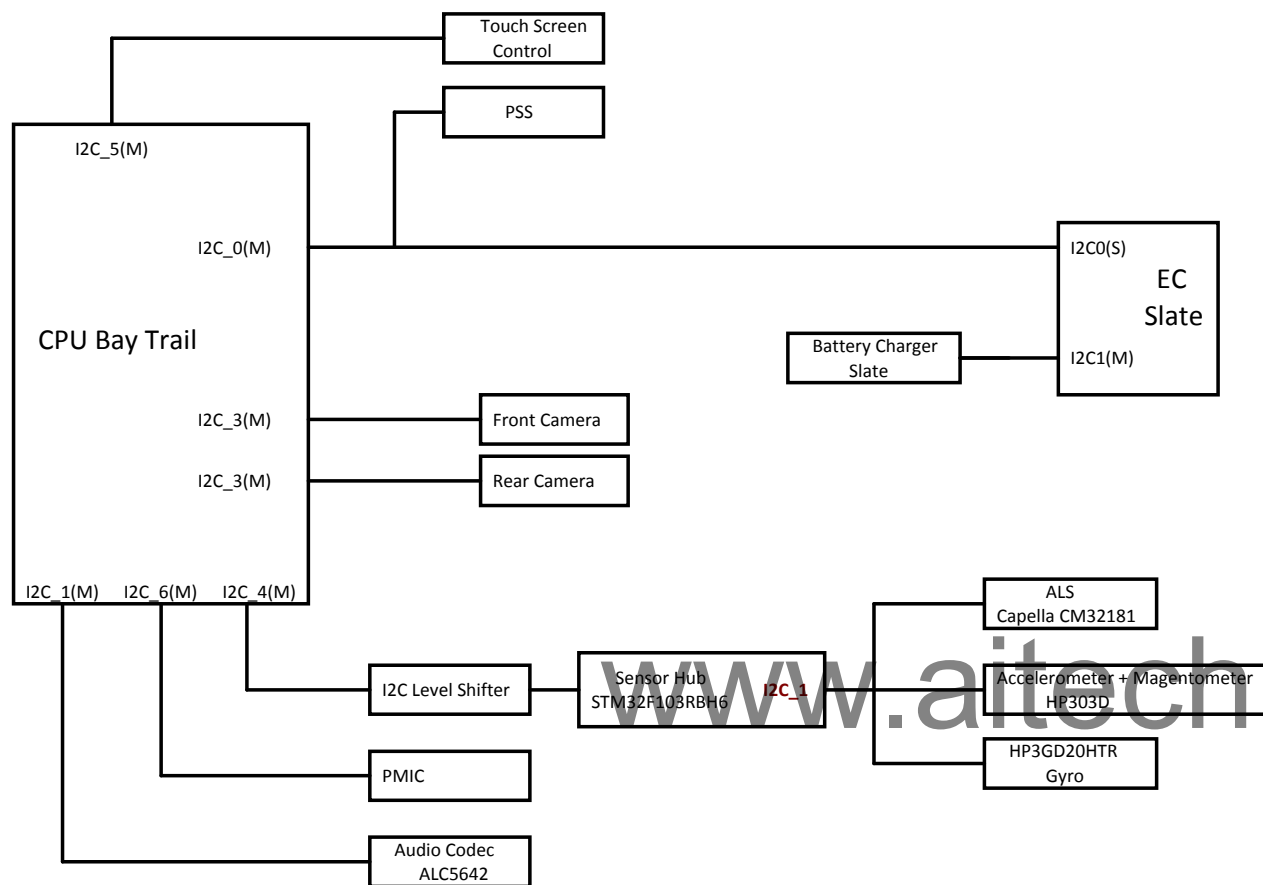


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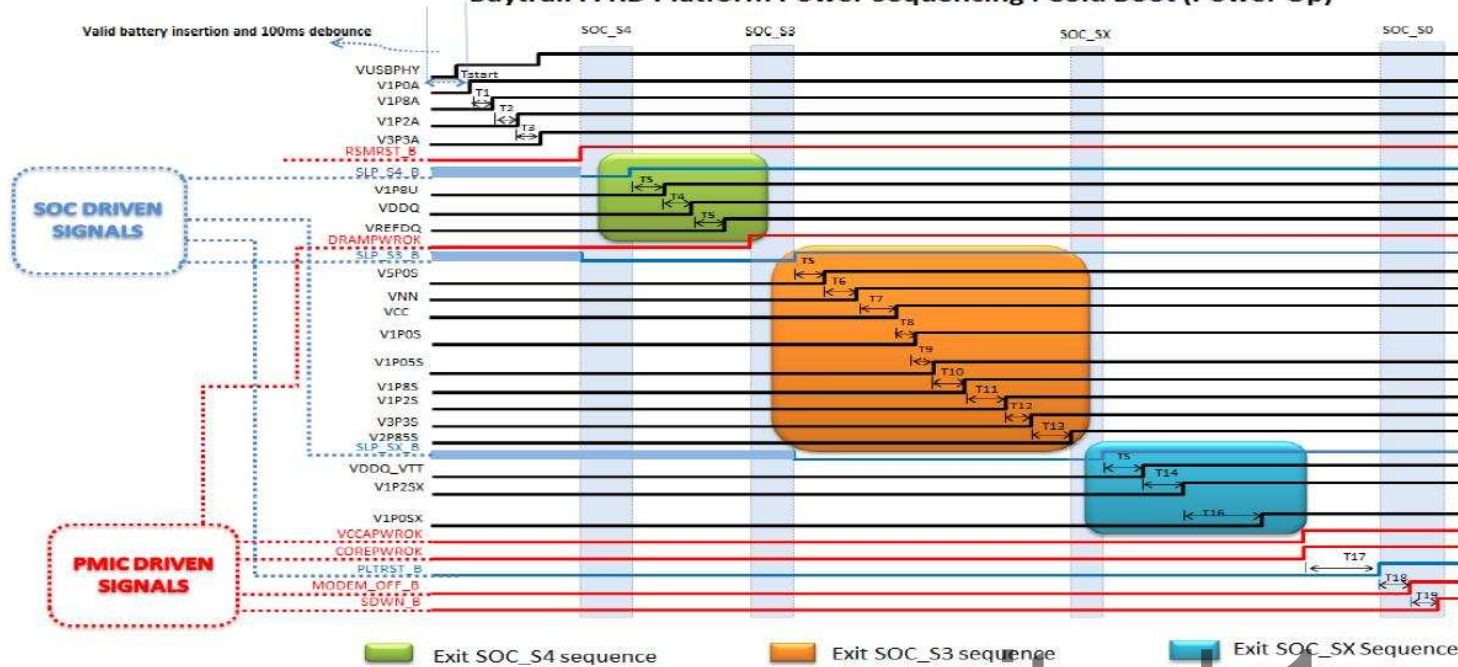
PROJECT : Bulldog

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Baytrail FFRD Platform Power Sequencing : Cold Boot (Power Up)



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				1A

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